| Hall Ticket No: | | | | | Question Paper Code: 14ENG104 |
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MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE

(UGC-AUTONOMOUS)

MCA I Year I Semester (R16) Supplementary End Semester Examinations – January 2020 (Regulations: R16)

ENGLISH FOR COMMUNICATION

| Time: 3Hrs Max Marks: 5 | | | | | | | |
|-------------------------|---|-----|--|--|--|--|--|
| A | Attempt all the questions. All parts of the question must be answered in one place only. In Q.no 1 to 5 answer either Part-A or B only | | | | | | |
| Q.1(A) | What are idiomatic sentences? Write 10 sentences by using different idioms. | 10M | | | | | |
| | OR | | | | | | |
| Q.1(B) | What is the use of prepositions? Write 5 prepositions with their usage.5M What are Word Roots? Write their usage with examples.5M | 10M | | | | | |
| Q.2(A) | What is Body Language? Give examples on the purpose of better Body Language in one's Professional Career. | 10M | | | | | |
| | OR | | | | | | |
| Q.2(B) | "Communication is a two way process of exchanging ideas or information between two human beings". Explain this statement with the help of a diagram. | 10M | | | | | |
| Q.3(A) | How can good listening habits be developed? 5M What are the Jargons of speech? 5M | 10M | | | | | |
| | OR | | | | | | |
| Q.3(B) | "The speaking and public presentation skills are primary requirements for a successful leader". Discuss the statement. | 10M | | | | | |
| Q.4(A) | Mention the Pre Interview and Post Interview strategies. | 10M | | | | | |
| | OR | | | | | | |
| Q.4(B) | What are the prerequisites of effective presentations? | 10M | | | | | |
| Q.5(A) | Write an e-mail to the Principal Secretary of Higher education, A.P on the need of Students' Skill Development centers across Andhra Pradesh. | 10M | | | | | |
| Q.5(B) | Write a letter to the Chairman, Andhra Pradesh state Technical Education Council, to introduce the new courses like Artificial Intelligence, Machine Learning and Black Chain Technology in the new Curriculum. *** END*** | 10M | | | | | |

| Hall Ticket No: | | | | | | Question Paper Code: 16MCA10 |
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| Hall Ticket No: | | | | | | Question Paper Code: 16MCA1 |

MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE

(UGC-AUTONOMOUS)

MCA I Year I Semester (R16) Supplementary End Semester Examinations – January 2020 (Regulations: R16)

INTRODUCTION TO COMPUTING

| Time: 3 | BHrs Max Marl | ks: 50 | | | | | | | |
|-----------|---|--------|--|--|--|--|--|--|--|
| | Attempt all the questions. All parts of the question must be answered in one place only. In Q.no 1 to 5 answer either Part-A or B only | | | | | | | | |
| Q.1(A) | Draw the block diagram of a computer. Explain the function of each of the block along with its characteristics. | | | | | | | | |
| | OR | | | | | | | | |
| Q.1(B) | How to solve a problem using a computer? Explain with an example. | | | | | | | | |
| Q.2(A) | Explain about various physical devices used to construct Memories in detail. | 10M | | | | | | | |
| | OR | | | | | | | | |
| Q.2(B) | Explain the following in brief | 5M*2= | | | | | | | |
| | i) Structure of an Instruction. | 10M | | | | | | | |
| 0.2/4\ | ii) Machine Language. | 1014 | | | | | | | |
| Q.3(A) | Describe how data is read from I/O Units to Main Memory and written from Main Memory I/O Units in brief | 10M | | | | | | | |
| | OR | | | | | | | | |
| Q.3(B) | Outline the following in breif | 5M*2= | | | | | | | |
| | i) Interrupt Structure. | 10M | | | | | | | |
| E - / - \ | ii) Software Qualities. | | | | | | | | |
| Q.4(A) | Explain Various Data Models for DBMS in brief. | 10M | | | | | | | |
| | OR | | | | | | | | |
| Q.4(B) | Why Multi tasking? Explain main functions of OS Multitasking in detail. | 10M | | | | | | | |
| Q.5(A) | Explain about LAN Technologies and how LAN Works. | 10M | | | | | | | |
| | OR | | | | | | | | |
| Q.5(B) | Write a note on the following in brief. | 5M*2= | | | | | | | |
| | i) TCP/IP | 10M | | | | | | | |
| | ii) World Wide Web(WWW) *** END*** | | | | | | | | |
| | END | | | | | | | | |

| Hall Ticket No: | | | | | | Question Paper Code: 16MCA102 |
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MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE

(UGC-AUTONOMOUS)

MCA I Year I Semester (R16) Supplementary End Semester Examinations - Jan 2020 (Regulations: R16)

PROGRAMMING IN C

| Hall Tick | et No: Question Paper Code: 16MC/ | A103 | | | | | | | | |
|------------|---|-------------|--|--|--|--|--|--|--|--|
| MA | MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE (UGC-AUTONOMOUS) | | | | | | | | | |
| MCA | MCA I Year I Semester (R16) Supplementary End Semester Examinations – Jan' 2020 (Regulations: R16) | | | | | | | | | |
| | COMPUTER ORGANIZATION | | | | | | | | | |
| Time: 3F | | | | | | | | | | |
| | Attempt all the questions. All parts of the question must be answered in one place only. In Q.no 1 to 5 answer either Part-A or B only | | | | | | | | | |
| Q.1(A) | (A) With the help of neat diagram, explain the basic structure of a digital computer. | | | | | | | | | |
| | OR | | | | | | | | | |
| Q.1(B) | i) An encoder performs the inverse operation of a decoder." Explain. | | | | | | | | | |
| | ii) What is the functional difference between a NAND gate and a negative-OR gate? Do they both have the same truth table? | | | | | | | | | |
| Q.2(A) | List and explain the characteristics of CISC and RISC | | | | | | | | | |
| | OR | | | | | | | | | |
| Q.2(B) | What is register? Explain the different types of registers. | 10M | | | | | | | | |
| Q.3(A) | With the help of neat diagram, explain the bus architecture. | 10M | | | | | | | | |
| | OR | | | | | | | | | |
| Q.3(B) | Explain the following: | 10M | | | | | | | | |
| | i) Instruction format | | | | | | | | | |
| B. Charles | ii) hand shaking | | | | | | | | | |
| Q.4(A) | The access time of a cache memory is 100 ns and that of main memory is 1000 ns. It is | 10M | | | | | | | | |
| | estimated that 80% of the memory requests are for read and the remaining 20% are | | | | | | | | | |

for write. The hit ratio for read accesses only is 0.9. A write-through procedure is used.

(i). What is the average access time of the system considering only memory read cycles?

(ii). What is the average access time of the system for both read and write requests?

OR

Q.4(B) Distinguish between the virtual memory and cache memory. Write the merits and demerits of virtual memory.

10M

Q.5(A) Why are the read and write control lines in a DMA controller bidirectional? Under what condition and for what purpose are they are they used as inputs? Under what condition and for what purpose are they are they used as outputs?

10M

OR

Q.5(B) What is interface? Explain the different types of interfaces with examples.

10M

*** END***